



AMD Instinct™ MI325X Firmware Changes

Publication Number: 68117

v1.0 Release F25.03.03

Date: August 2025

Contents

Chapter 1: Introduction	3
Chapter 2: Release Contents	4
Chapter 3: Enhancements	5
Chapter 4: Key Fixes	6
Appendix A: Notices	7
Appendix B: Trademarks	7

List of Tables

Table 2-1 Firmware Bundle Component Versions	4
Table 3-1 Enhancements	5
Table 4-1 Fixed Known Issues	6
Table 4-2 Fixed New Issues	6



Chapter 1: Introduction

This document provides release notes for the AMD Instinct™ MI325X accelerator firmware, including enhancements and key fixes.

Chapter 2: Release Contents

The following table details the versions of the constituent firmware and software components in this release. The **Previous Component Details** are included for reference and represent the component details from the previous release. For convenience, any versions that have changed since the previous release are highlighted. Some versions may remain unchanged across different releases.

Table 2-1 Firmware Bundle Component Versions

Location	Component	Component Details	Previous Component Details	Version Number
Bundle	ROT	00770000 (Production)	00770000 (Production)	0x00770000
Bundle	IFWI	01.07.159251	01.05.154887	0x10726E13
Bundle	SMC	2.11.3.28.0F (Production)	2.11.3.26.0F (Production)	0x01031C0F
Bundle	SMC FPGA	00.26.30	00.26.30	0x00026030
Bundle	UBB FPGA	00.26.30	00.26.30	0x00026030
Bundle	Retimer	2.11.062 2.11.075 2.11.076 2.11.077 2.11.078 2.11.083	2.11.062 2.11.075 2.11.076 2.11.077 2.11.078 2.11.083	0x0200B03E 0x0200B04B 0x0200B04C 0x0200B04D 0x0200B04E 0x0200B053
Bundle	Remote Management	4.4.07	4.4.04	0x04004007
Bundle	OAM VR	05.0.32 (MPS) 04.0.34 (IFX)	05.0.32 (MPS) 04.0.33 (IFX)	05.0.32 04.0.34
Bundle	UBB VR	04.0.7 (MPS) 03.0.7 (IFX)	04.0.7 (MPS) 03.0.7 (IFX)	04.07 03.07

Chapter 3: Enhancements

If there are enhancements, the following table details them, organized by domain.

Table 3-1 Enhancements

AMD Ref ID	Domain	Description
P0647	General	Added support for SMC to in-band PLDM bundle version reporting.
P0715	IFWI	EDP Scalar Support (Functional enablement only)

Chapter 4: Key Fixes

The tables below detail the key fixes in this release, if present, sorted by domain. The "Fixed Known Issues" table includes previously documented issues that have been resolved in this release. The "Fixed New Issues" table lists issues that were not previously documented but have been resolved in this release.

Table 4-1 Fixed Known Issues

AMD Ref ID	Domain	Issue	Fix
P171349	RAS	UMC CRC read/write error injection failure on first attempt	Fixed registers access issue in FW
P173370	Remote Management	eFuse faults can lead to generating subsequent CTFs	Add check for eFuse faults and ensure false CTFs are not generated because of this type of fault scenario
U2011	SMC	Critical OAM VR sensor events	Updated the SMC's VR access logic to use VR device access logic that is compatible with the VR device's design. Incorrect access logic resulted in undefined data being reported.
P174242	Virtualization	Security violation message observed when loading guest ROCm driver	Fixed invalid security policy bit that causing the violation message.

Table 4-2 Fixed New Issues

AMD Ref ID	Domain	Issue	Fix
P174452	IFWI	GCM Sdp RdRsp/WrRsp tag underflow errors	Optimization of settings in the PMFW
P177583 P178422	Performance	Increased latency and reduced bandwidth during traffic contention in a MultiNode Setup	Fix within the Universal Boot Loader (UBL)FW to enable relaxed ordering.
P173362	RAS	Failure to deliver mode2reset error message/code -62	Fix in the PMFW to resolve timing dependencies during resets between AIDs
P180638	Stress	Voltage related PGOOD Faults during Pytorch workloads	Settings change in IFX VR 0.34 to improve current limit exit overshoot.



Appendix A: Notices

© Copyright 2025 Advanced Micro Devices, Inc.

The information presented in this document is for informational purposes only and may contain technical inaccuracies, omissions, and typographical errors. The information contained herein is subject to change and may be rendered inaccurate for many reasons, including but not limited to product and roadmap changes, component and motherboard version changes, new model and/or product releases, product differences between differing manufacturers, software changes, BIOS flashes, firmware upgrades, or the like. Any computer system has risks of security vulnerabilities that cannot be completely prevented or mitigated. AMD assumes no obligation to update or otherwise correct or revise this information.

However, AMD reserves the right to revise this information and to make changes from time to time to the content hereof without obligation of AMD to notify any person of such revisions or changes.

THIS INFORMATION IS PROVIDED "AS IS." AMD MAKES NO REPRESENTATIONS OR WARRANTIES WITH RESPECT TO THE CONTENTS HEREOF AND ASSUMES NO RESPONSIBILITY FOR ANY INACCURACIES, ERRORS, OR OMISSIONS THAT MAY APPEAR IN THIS INFORMATION.

AMD SPECIFICALLY DISCLAIMS ANY IMPLIED WARRANTIES OF NON-INFRINGEMENT, MERCHANTABILITY, OR FITNESS FOR ANY PARTICULAR PURPOSE. IN NO EVENT WILL AMD BE LIABLE TO ANY PERSON FOR ANY RELIANCE, DIRECT, INDIRECT, SPECIAL, OR OTHER CONSEQUENTIAL DAMAGES ARISING FROM THE USE OF ANY INFORMATION CONTAINED HEREIN, EVEN IF AMD IS EXPRESSLY ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

Appendix B: Trademarks

AMD, the AMD Arrow logo, and combinations thereof are trademarks of Advanced Micro Devices, Inc.

Other product names used in this publication are for identification purposes only and may be trademarks of their respective companies.